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Applicant: Randy Bonella et al.

Art Unit: 2115

Serial No.: 09/662,054

Examiner: Dennis M. Butler

Filed

: September 15, 2000

Title

: DIGITAL SYSTEM OF ADJUSTING DELAYS ON CIRCUIT BOARDS

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Attached to this facsimile communication cover sheet is Response, faxed this 21st day of May, 2004, to the United States Patent and Trademark Office.

Respectfully submitted,

Date: May 21, 2004

Reg. No. 32,030

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